

FIG. 1 is a flowchart of a prior art process for forming a conductive layer on a substrate.

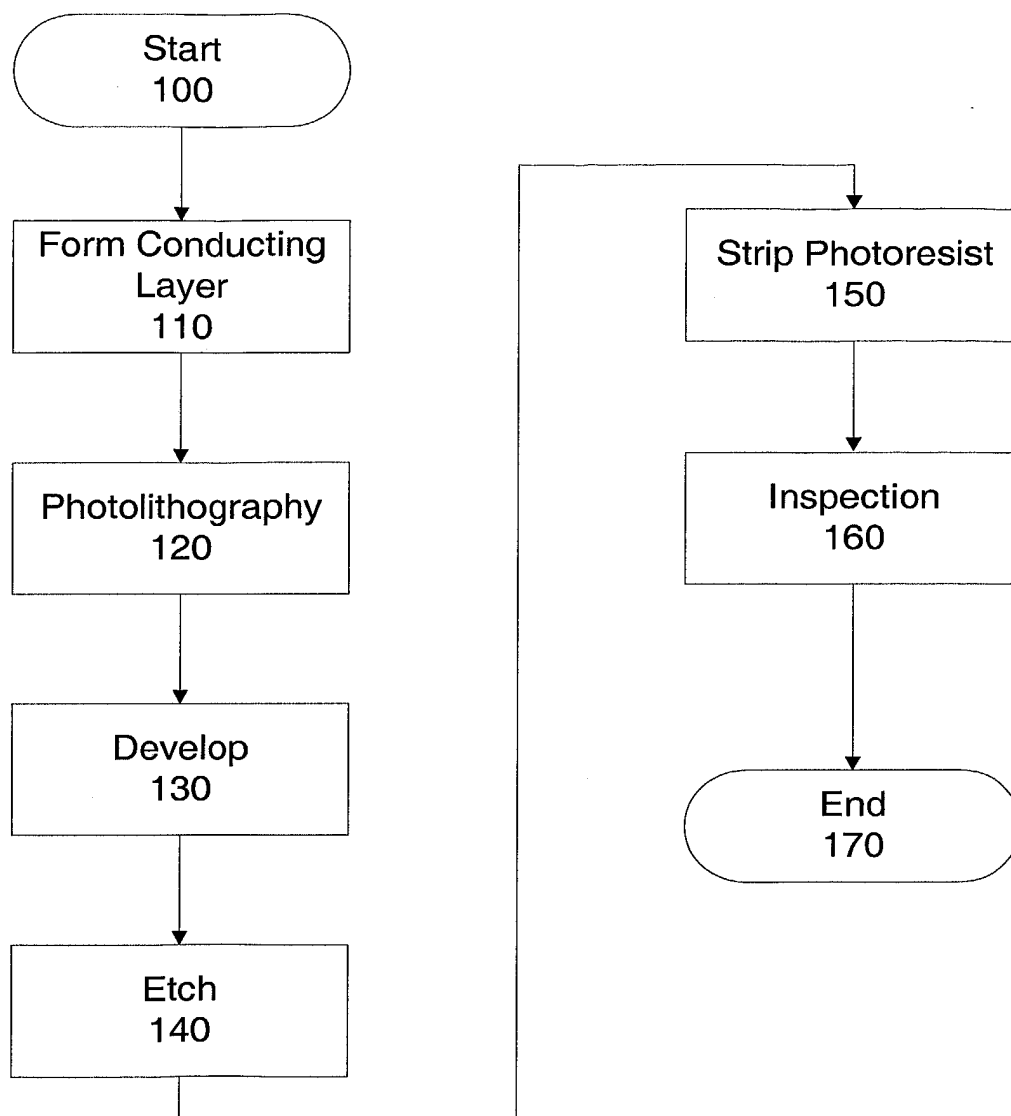


Figure 1 (Prior Art)

10653810.0183

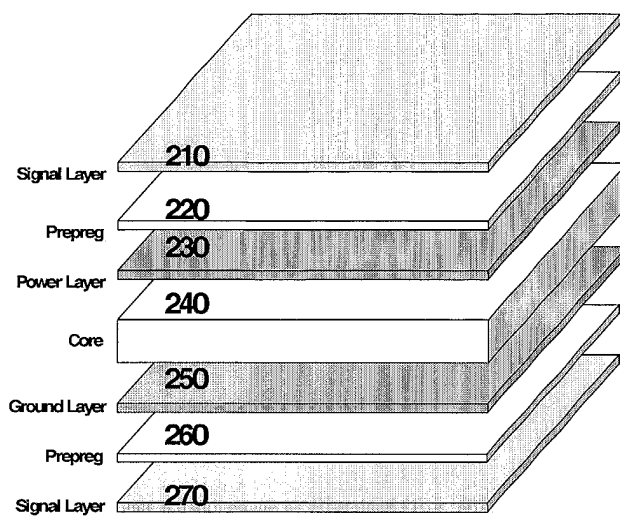


Figure 2 (Prior Art)

105310.01300

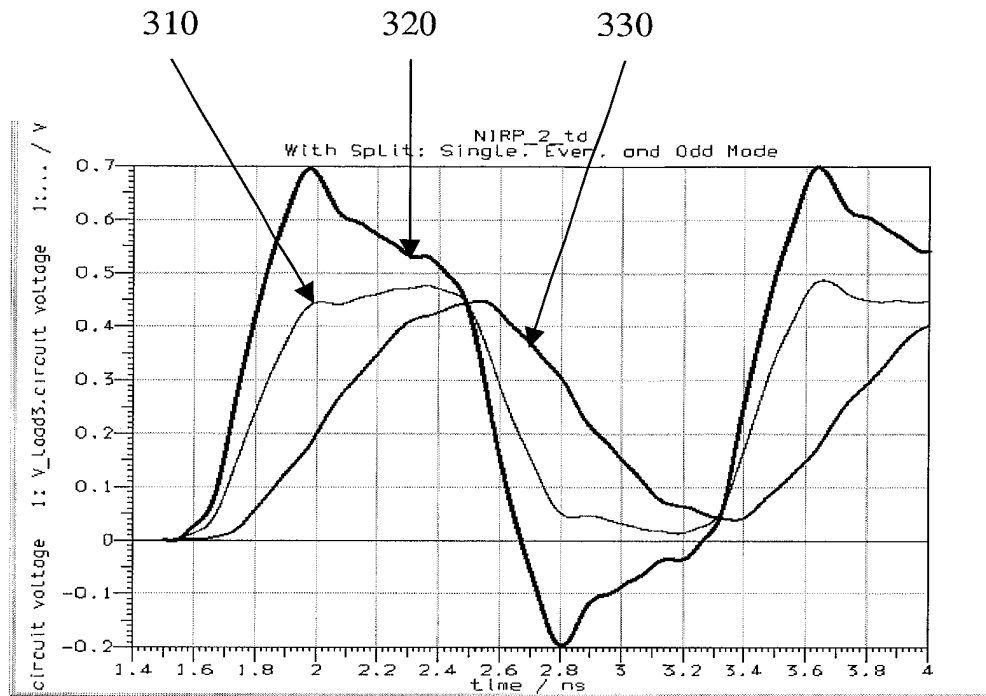


Figure 3 (Prior Art)

FIG. 4

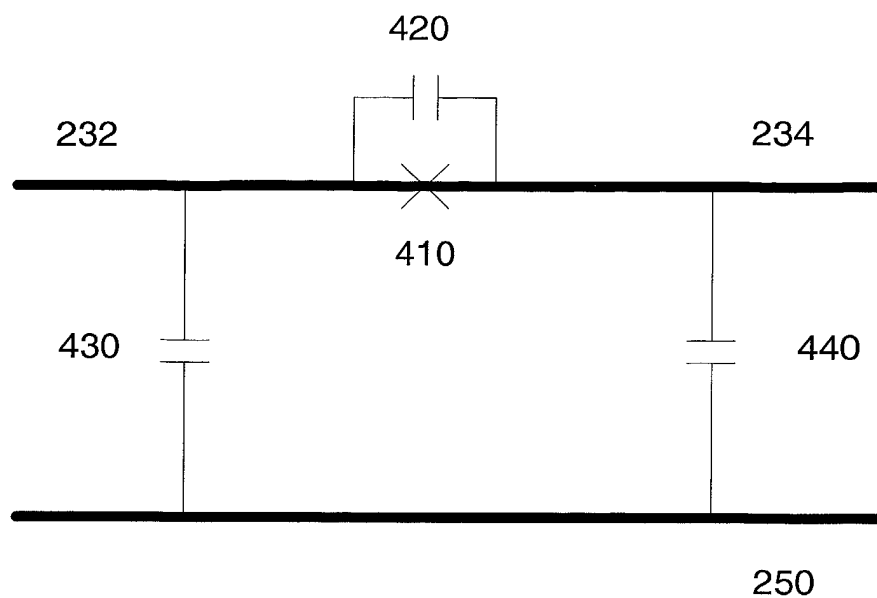


Figure 4 (Prior Art)

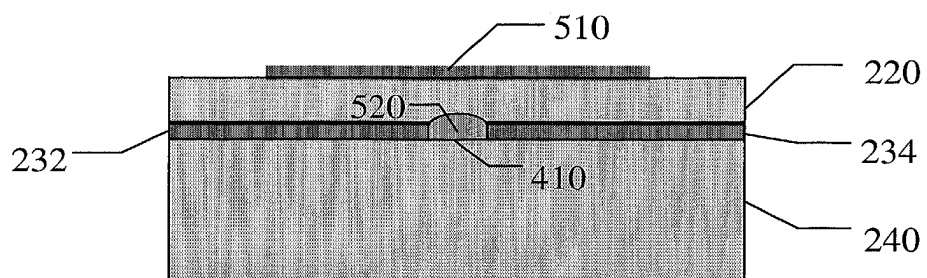


Figure 5

1003610-01BEE5001

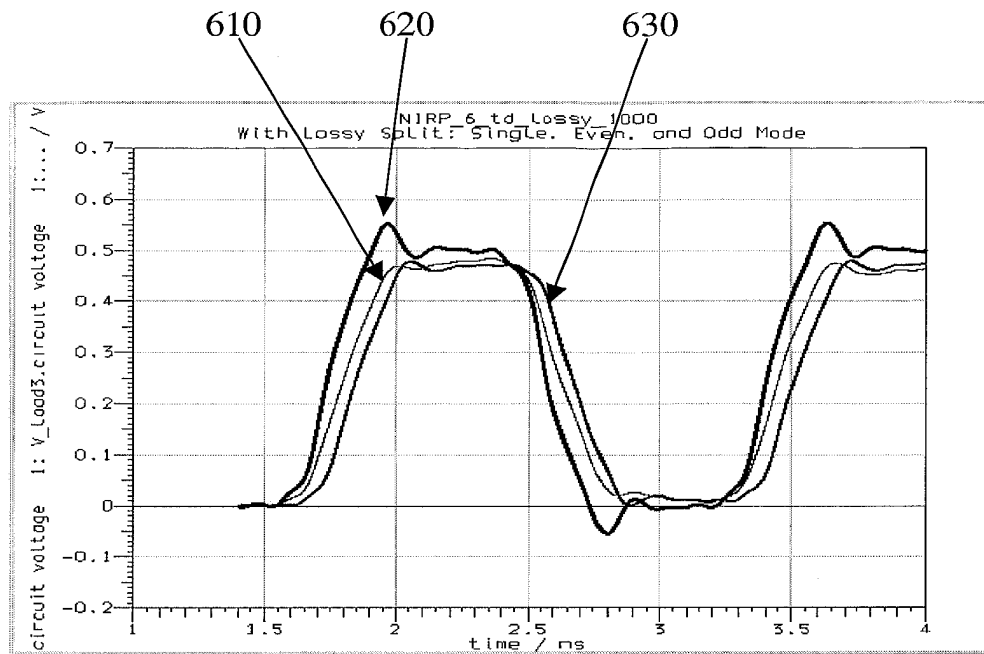


Figure 6

1053810.011802

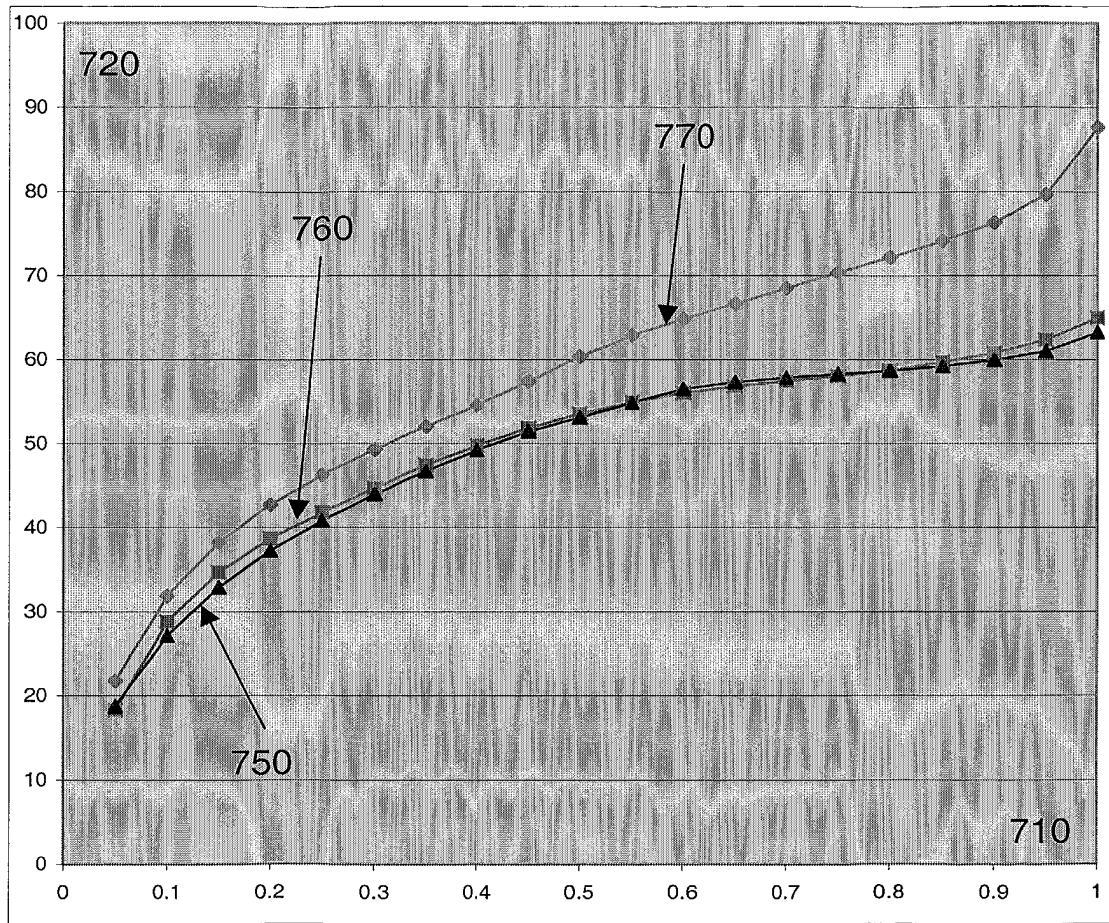


Figure 7

106340.0433

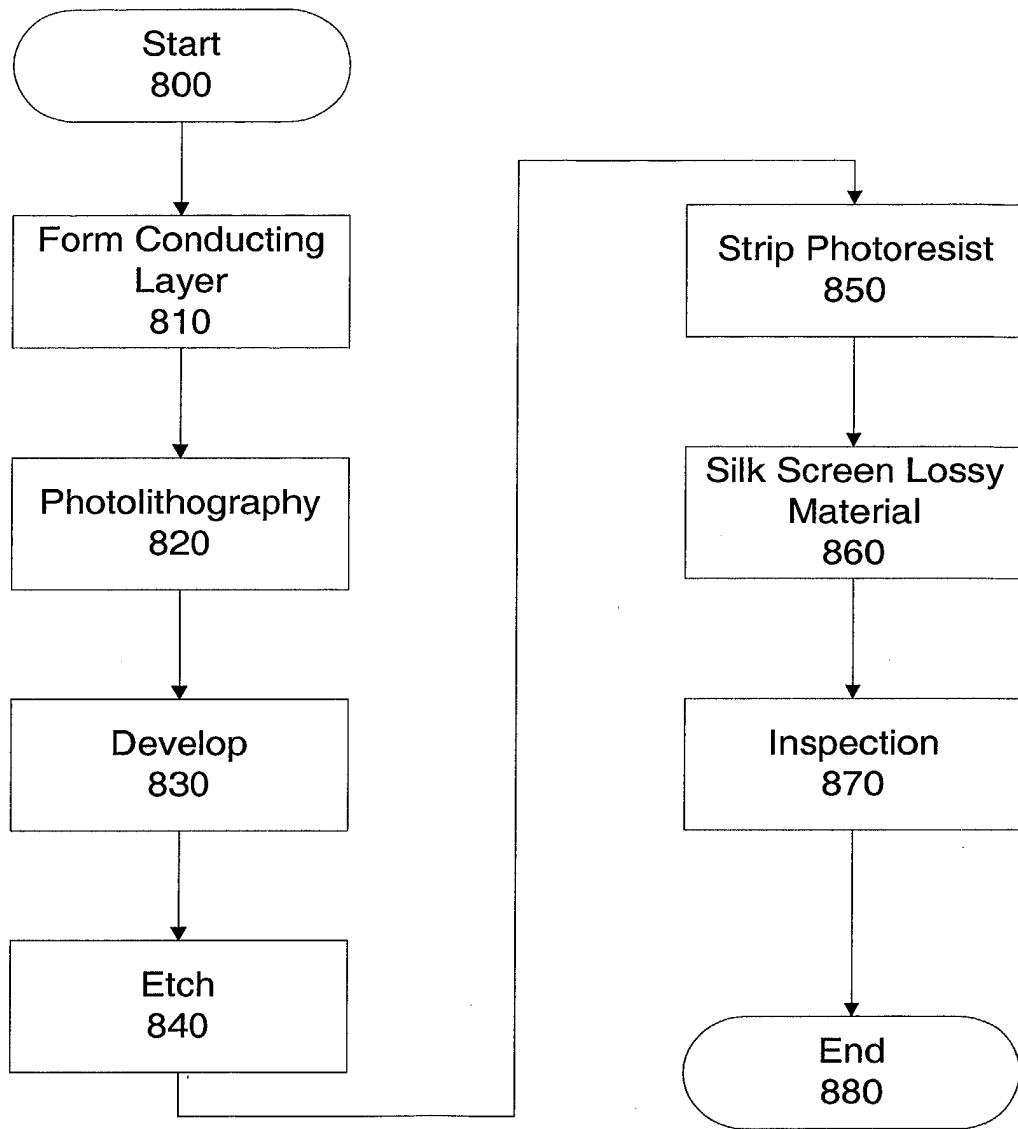


Figure 8